

UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION

PATENT NO. : 7,105,884 B2
APPLICATION NO. : 09/810595
DATED : September 12, 2006
INVENTOR(S) : Coursey

Page 1 of 2

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Title Page 2, U.S. PATENT DOCUMENTS, please insert --5,396,093 3/1995 Lu--
before "5,714,779".

Title Page 2, U.S. PATENT DOCUMENTS, please insert --5,933,726 8/1999
Nishimura et al.-- before "5,990,021".

Title Page 2, U.S. PATENT DOCUMENTS, please insert --6,015,983 1/2000
Parekh-- before "6,030,879".

Title Page 2, U.S. PATENT DOCUMENTS, please insert --6,114,201 9/2000 Wu--
before "6,130,126".

Title Page 2, U.S. PATENT DOCUMENTS, please insert --6,423,609 7/2002 Van
Itallie-- before "6,426,243".

Title Page 2, U.S. PATENT DOCUMENTS, please insert --6,498,375 12/2002
Dennison
6,589,876 7/2003 Tran
6,617,631 9/2003 Huang-- before "6,734,479 B1".

Title Page 2, OTHER PUBLICATIONS, please insert -- Wolf, *Silicon Processing For
The VLSI Era, Vol. 2: Process Integration*, Lattice Press, pp. 238 (1990)-- before "*
cited by examiner".

Col. 3, line 54, please delete "to" after "produce"

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Col. 4, line 29, please delete "be" after "preferably" and insert --by--.

Signed and Sealed this

Nineteenth Day of June, 2007

A handwritten signature in black ink, appearing to read "Jon W. Dudas". The signature is stylized with a large, looped initial "J" and a distinct "D" at the end.

JON W. DUDAS
Director of the United States Patent and Trademark Office